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				Application Number	09/924,274
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				First Named Inventor	Paul Metzgen
				Art Unit	2825
				Examiner Name	Vuthe Siek
Sheet	1	of	1	Attorney Docket Number	174/221 (A741)

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		6,871,341 B1	03/22/2005	Shyr	Duplication

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FOREIGN PATENT DOCUMENTS					
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NON PATENT LITERATURE DOCUMENTS					
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VS		James B. Peterson et al. "Scheduling and Partitioning ANSI-C Programs onto Multi-FPGA CCM Architectures", IEEE, pp. 178-187, 1996.			
VS		Apostolos A. Kountouris et al., "High Level Pre-Synthesis Optimization Steps using Hierarchical Conditional Dependency Graphs", IEEE, pp. 290-294, 1999.			

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